

Amendments to the Specification:

Page 8, line 10, ¶ [0025], amend as follows:

While ~~FIG. 3~~ FIG. 2 shows the controller 302 and the rights assignment engine 310 as separate units, the functions performed by these units may be combined in one processor or may be further divided among plural processors such as digital signal processors and/or performed by dedicated hardware such as application specific integrated circuits (ASIC), e.g., hardwired electronic or logic circuits or programmable logic devices., or other hardware or software implementations.